

BACHELOR OF ENGINEERING IN ELECTRONIC ENGINEERING LC_EECEN_D YEAR 3				
EXAM TIMETABLE EECEN_D_Y3				
DAY	DATE	SUBJECT	TIME	LOCATION
Monday	21st Aug 2017	Electronics TRON 07010 (Sem 1)	09:30 - 12:00	8A5.01 - 5.05
Monday	21st Aug 2017	Microcontroller Fund. & Digital I/O DIGL 07001 (Sem 1)	14:30 - 17:00	8A5.01 - 5.05
Tuesday	22nd Aug 2017	Engineering Mathematics 3 MATH 07015 (Sem 1)	09:30 - 12:00	8A5.01 - 5.05
Tuesday	22nd Aug 2017	Labview & Data Acquisition PROG 07020 (Sem 1)	14:30 - 17:00	8A5.01 - 5.05
Wednesday	23rd Aug 2017	Probability & Statistics STAT 07002 (Sem 2)	09:30 - 12:00	8A5.01 - 5.05
Wednesday	23rd Aug 2017	Power Electronics TRON 07011 (Sem 2)	14:30 - 17:00	8A5.01 - 5.05
Thursday	24th Aug 2017	Digital Communications COMM 07003 (Sem 2)	09:30 - 12:00	8A5.01 - 5.05
Thursday	24th Aug 2017	Adv. Networking & Internet Technology HNET 07011 (Sem 2)	14:30 - 17:00	8A5.01 - 5.05
Friday	25th Aug 2017	Microcontroller Interfacing & Analogue I/O DIGL 07002 (Sem 2)	09:30 - 12:00	8A5.01 - 5.05

Please note there will be no admittance to Examination Halls without Photo I.D.

Please note that your K NUMBER is your examination number